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CONFIRMATION NO. 3942

<b>SERIAL NUMBER</b> 10/562,074	<b>FILING or 371(c) DATE</b> 04/12/2007 <b>RULE</b>	<b>CLASS</b> 324	<b>GROUP ART UNIT</b> 2858	<b>ATTORNEY DOCKET NO.</b> DE03 0214 US1		
<b>APPLICANTS</b> Martin Kadner, Hamburg, GERMANY; <b>** CONTINUING DATA *****</b> This application is a 371 of PCT/IB04/50927 06/17/2004 TSD <b>** FOREIGN APPLICATIONS *****</b> EUROPEAN PATENT OFFICE (EPO) 031018708 06/25/2003 TSD <b>** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **</b> 06/13/2007						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/TIMOTHY J DOLE/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance TJD Initials	<b>STATE OR COUNTRY</b> GERMANY	<b>SHEETS DRAWINGS</b> 1	<b>TOTAL CLAIMS</b> 2	<b>INDEPENDENT CLAIMS</b> 2
<b>ADDRESS</b> NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131 UNITED STATES						
<b>TITLE</b> Method and Circuit Arrangement for the Self-Testing of a Reference Voltage in Electronic Components						
<b>FILING FEE RECEIVED</b> 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees		
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